

**BEUSSE BROWNLEE WOLTER
MORA & MAIRE, P.A.**

FACSIMILE TRANSMITTAL SHEET

TO

FROM

James H. Beusse

Art Unit: 2826

Examiner: Fazli Erdem

COMPANY

U.S. Patent Office

DATE

February 24, 2005

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SENDER'S REFERENCE NUMBER:

075903-033

RE:

09/967,094

YOUR REFERENCE NUMBER

☒ URGENT ☐ FOR REVIEW ☐ PLEASE COMMENT ☐ PLEASE REPLY ☐ PLEASE RECYCLE

NOTES/COMMENTS

**RESPONSE TO OFFICE ACTION OF JANUARY 4,
2005.**

Please confirm receipt.

THANKS.

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Application No. 09/967,094

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

First Named Inventor: Siddhartha Bhowmik

Application No.: 09/967,094

Filed: 09/28/2001

Examiner: Fazli Erdem Art Unit: 2826

**For: Barrier Layer For Interconnect Structures
Of A Semiconductor Wafer and Method For
Depositing The Barrier Layer**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE AFTER FINAL REJECTION
UNDER 37 CFR 1.116

This paper is in response to the Final Rejection contained in the official Office Communication dated 01/04/2005, having a response due date of April 4, 2005.

Entry of this amendment is respectfully requested to place the application in condition for allowance, or in the alternative, to place the claims in better condition for consideration on appeal.

Please proceed to the following page.